Effective February 2016

NG/ND-Frames 320-1600A, 240-690V

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	IC01207016E10
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Note:

The following curves meet the requirements of UL, CSA, IEC, CCC and CE. The following circuit breakers are derived from Eaton, Westinghouse, or Cutler-Hammer history.

Time Current Curves are engineering reference documents for application and coordination purposes only.



Time Current Curves **TD012029EN** Effective February 2016

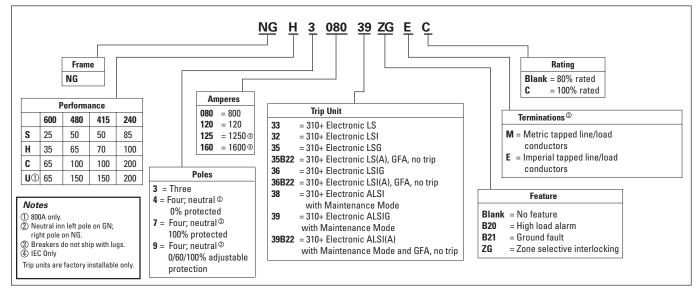
Note: Unless noted below, all curves remain unchanged from their prior revision.

Revision	Curve Number	Page	Date
Changed KAIC from 50 to 65 on page 3			
ZSI times added to short delay curves.		4 & 5	2/2016
Combined NG and ND frames into one o	locument.		
			_

Catalog Number Selection

This information is presented only as an aid to understanding catalog numbers. It is not to be used to build catalog numbers for circuit breakers or trip units.

Table 1. Series G N-Frame (320-1600A)



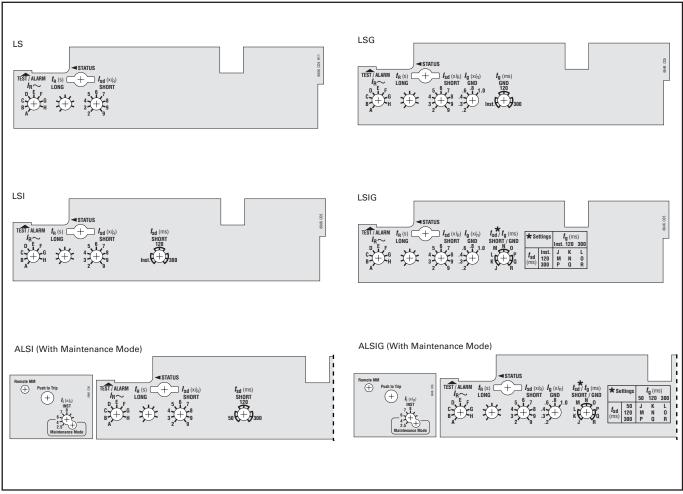


Figure 1. Digitrip 310+ Faceplates

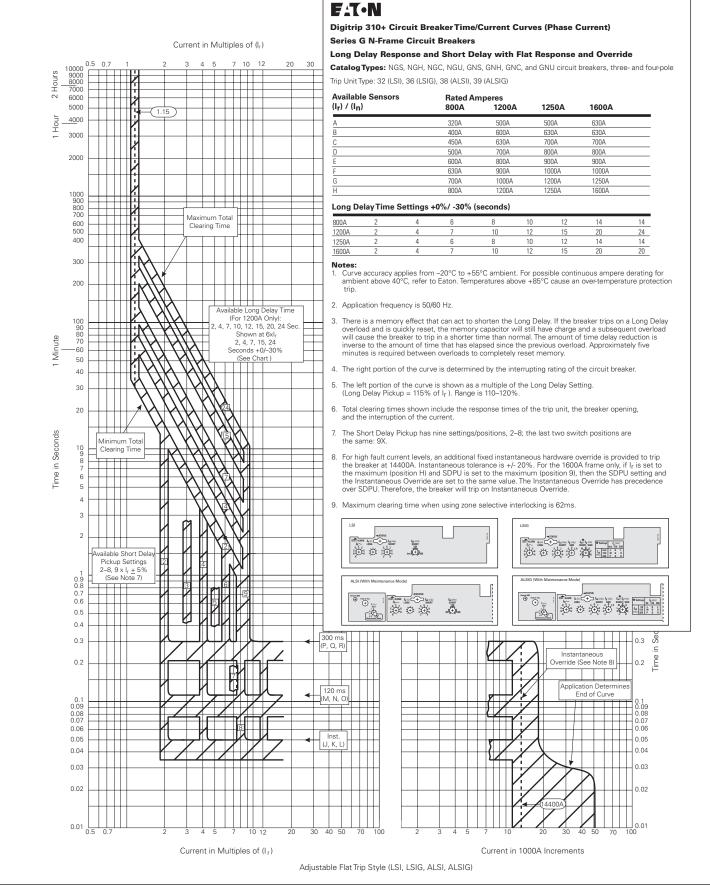


Figure 2. Digitrip 310+ Long Delay Response and Short Delay with Flat Response and Override Curve (LSI, LSIG, ALSI, ALSIG) - Curve Number TC01210010E, March 2012



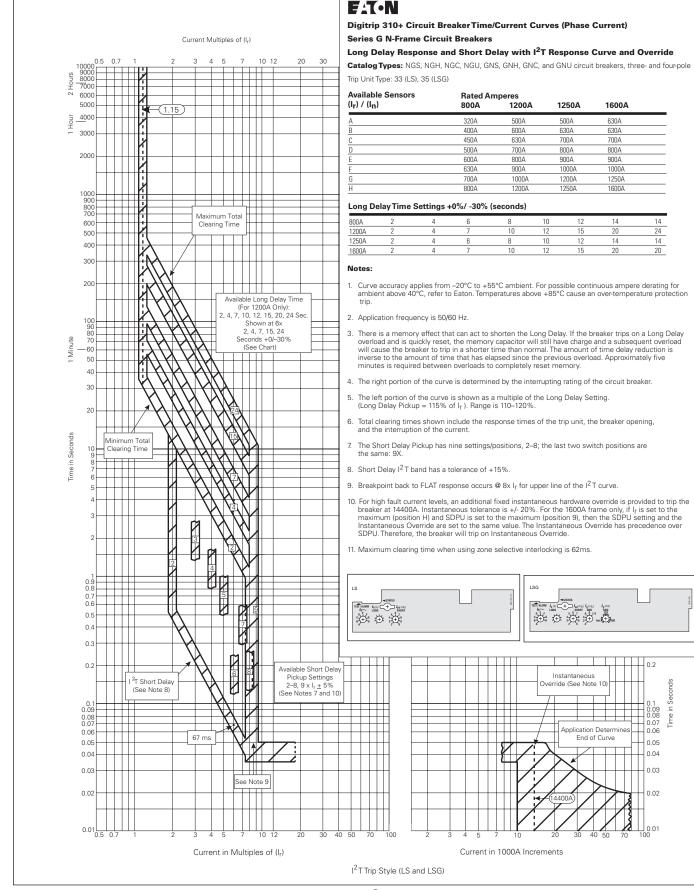


Figure 3. Digitrip 310+ Long Delay Response and Short delay with I²T Response Curve (LS, LSG) - Curve Number TC01210011E, March 2012

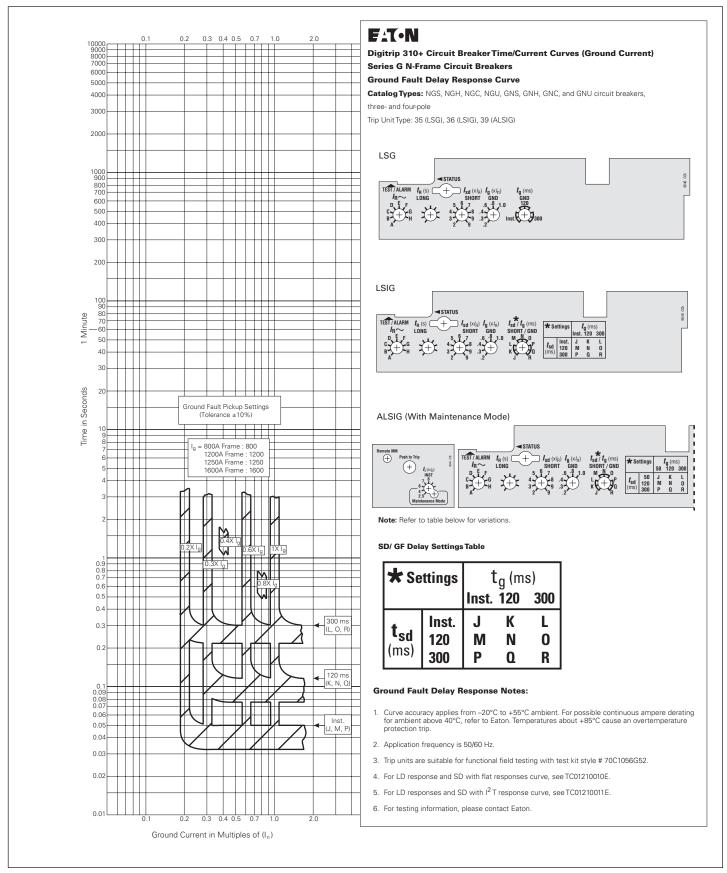


Figure 4. Ground Fault Delay Reponse Curve (LSG, LSIG, ALSIG) Curve Number TC01210012E, March 2012

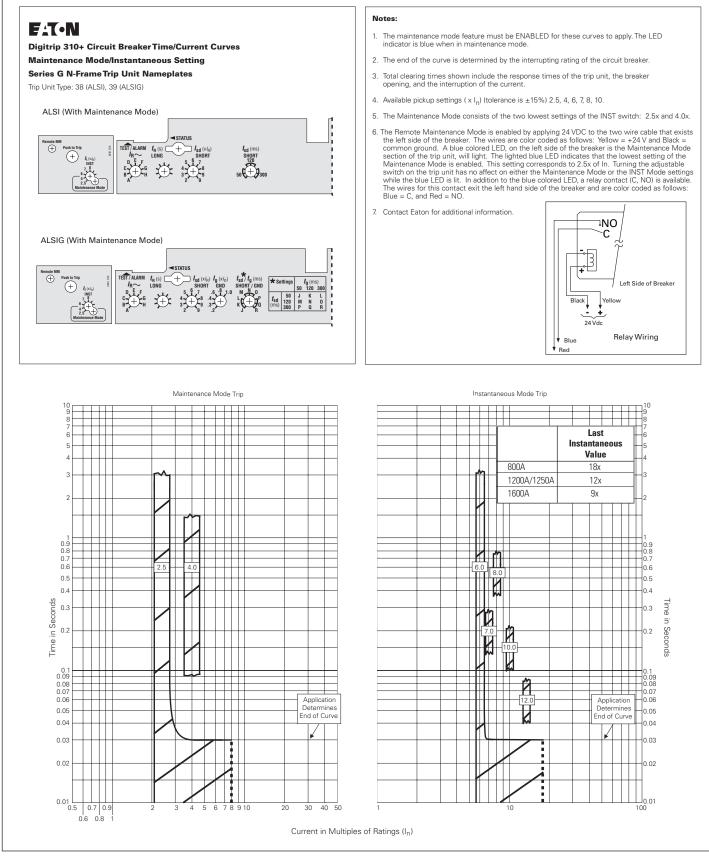


Figure 5. Maintenance Mode/Instantaneous Setting (ALSI, ALSIG) Curve Number TC01210016E ,TC01210017E, and TC 01210018E, March 2012

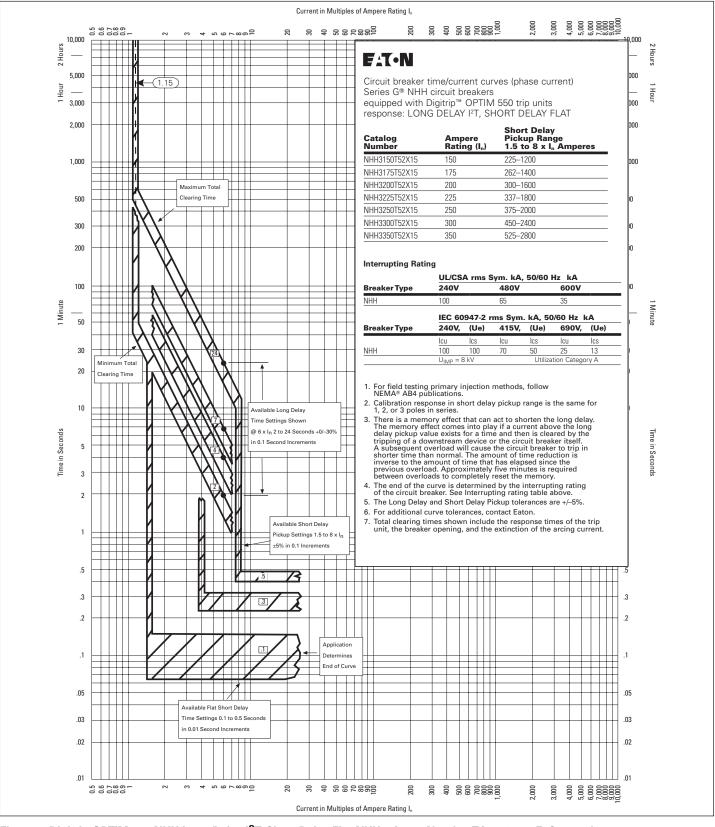


Figure 6. Digitrip OPTIM 550 NHH Long Delay I²T, Short Delay Flat NHH–Curve Number TC01207016E, September 2009

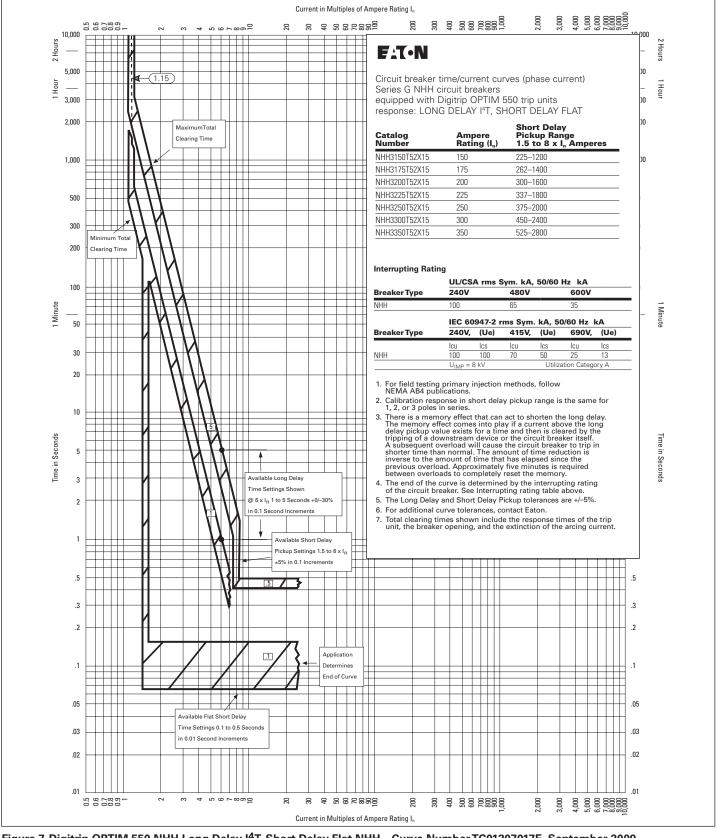
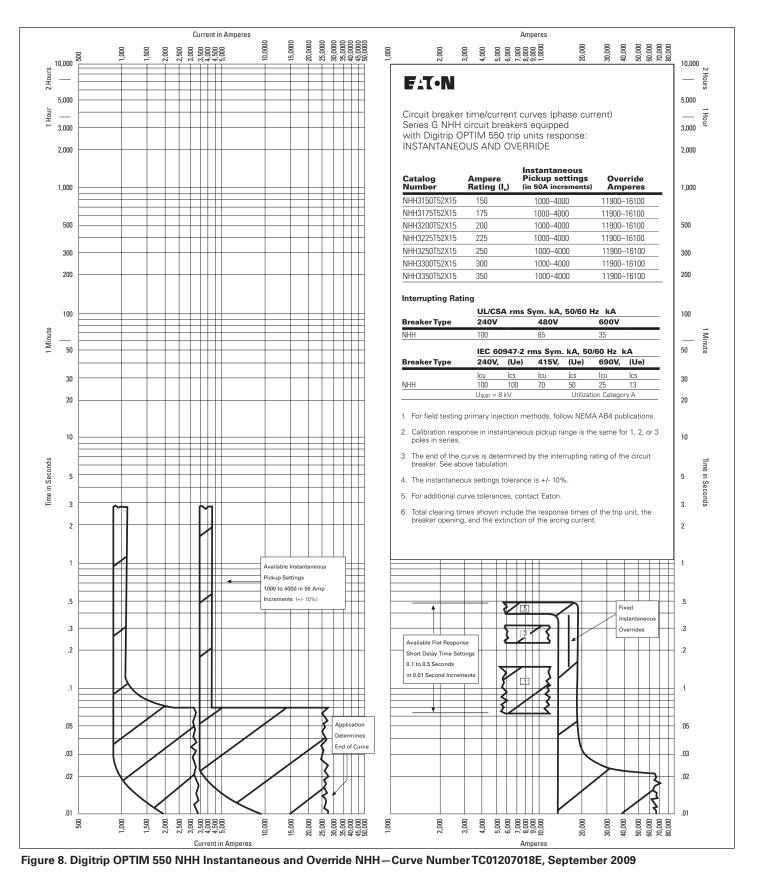


Figure 7. Digitrip OPTIM 550 NHH Long Delay I⁴T, Short Delay Flat NHH-Curve Number TC01207017E, September 2009



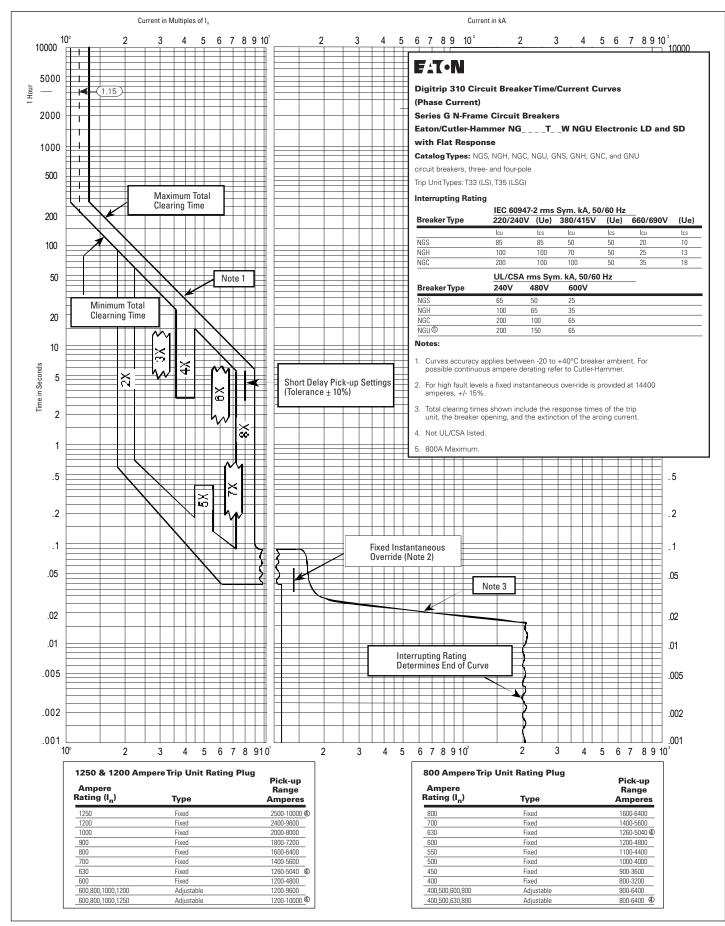


Figure 9. Digitrip 310 Long Delay and Short Delay with I2T Response (LS, LSG) - Curve Number TC01209003E

Time Current Curves **TD012029EN** Effective February 2016

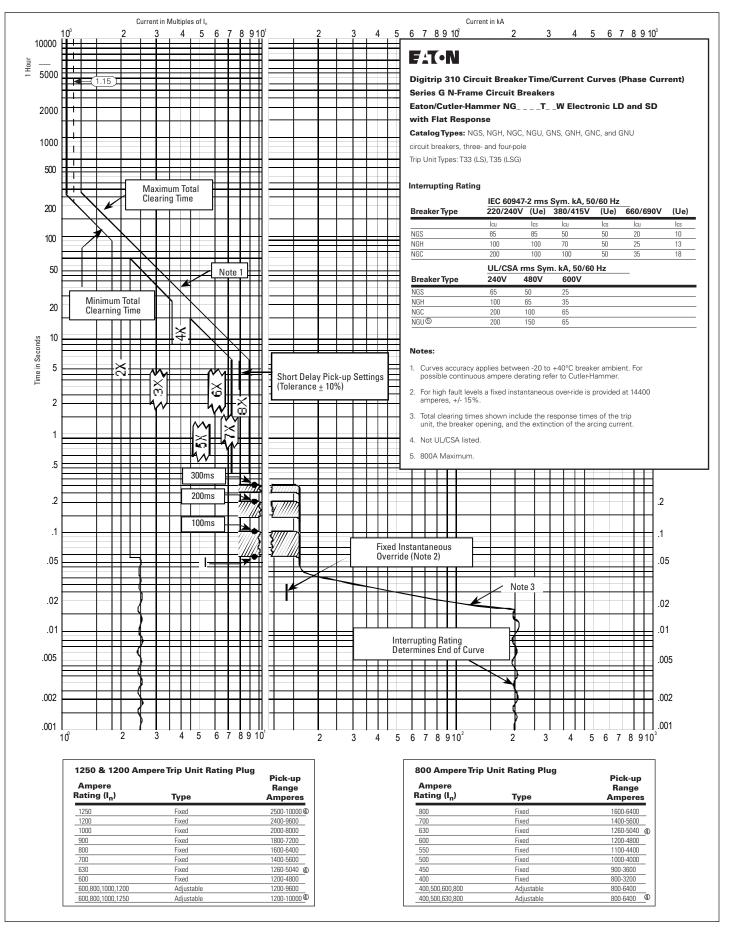


Figure 10. Digitrip 310 Long Delay and Short Delay with Flat Response (LSI, LSIG) - Curve Number TC01209004E

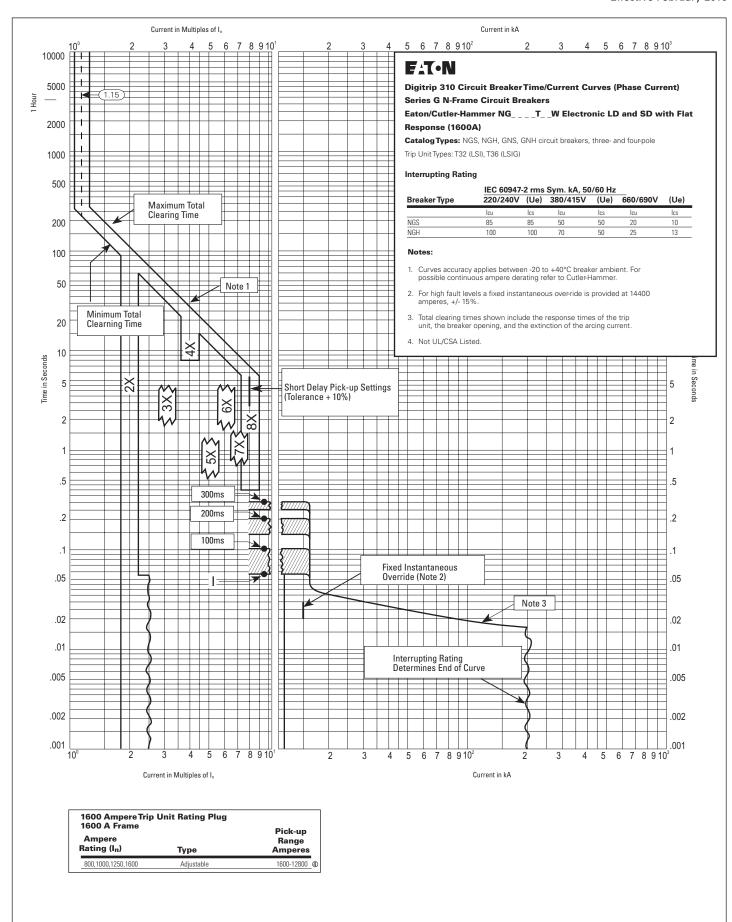
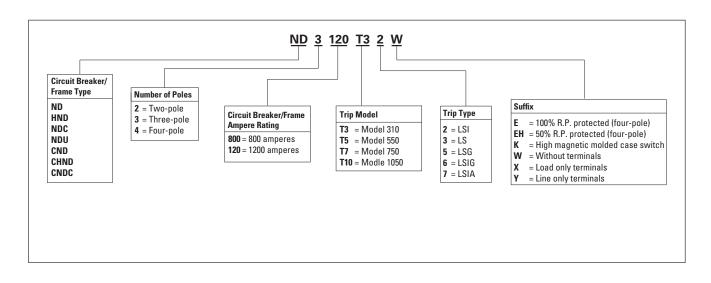


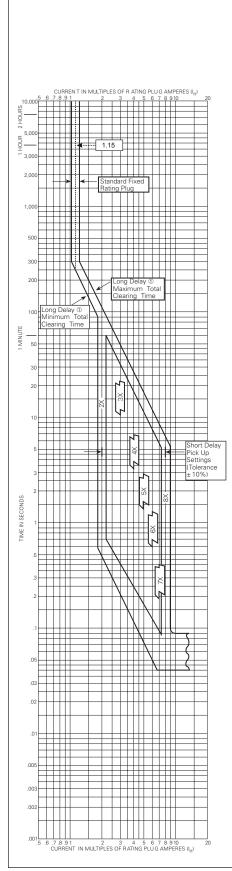
Figure 11. Digitrip 310 Long Delay and Short Delay with Flat Response (1600A) (LSI, LSIG) - Curve Number TC01209006E

Catalog Number Selection

This information is presented only as an aid to understanding catalog numbers. It is not to be used to build catalog numbers for circuit breakers or trip units.



Types ND, CND, HND, CHND, NDC, CNDC, NDU, NGU Equipped With Type NES Digitrip RMS 310 Trip Units With I²t Ramp Short Time Delay (Phase Protection)



F:T•N

Circuit Breaker Time/Current Curves (Phase Current)

Series C N-Frame Circuit Breakers

Equipped With Type NES Digitrip RMS 310 Trip Units

The NES Digitrip RMS 310 Trip Units are AC only devices that employ microprocessor based technology that provides true RMS current sensing means for proper correlation with thermal characteristics of conductors and equipment. They are used with Circuit Breaker Types: ND, CND, HND, CHND, NDC, and CNDC.



Available Rating Plugs

Frame Rating Amperes (Max.)	Ampere Rating (I _n)	Туре	Catalog Number	Short Delay Pickup Range Amperes
800	800	Fixed	8NES800T	1600-6400
800	700	Fixed	8NES700T	1400-5600
800	630	Fixed	8NES630T ④	1260-5040
800	600	Fixed	8NES600T	1200-4800
800	550	Fixed	8NES550T	1100-4400
800	500	Fixed	8NES500T	1000-4000
800	450	Fixed	8NES450T	900-3600
800	400	Fixed	8NES400T	800-3200
1200	400, 500, 600, 800	Adj.	A8NES800T1	800-6400
1200	400, 500, 600, 800	Adj.	A8NES800T2 ④	800-6400
1200	1200	Fixed	12NES1200T	2400-9600
1200	1000	Fixed	12NES1000T	2000-8000
1200	900	Fixed	12NES900T ④	1800-7200
1200	800	Fixed	12NES800T	1600-6400
1200	700	Fixed	12NES700T	1400-5600
1200	630	Fixed	12NES630T ④	1260-5040
1200	600	Fixed	12NES600T	1200-4800
1200	600, 800, 1000, 1200	Adj.	A12NES1200T1	1200-9600

Interrupting Rating

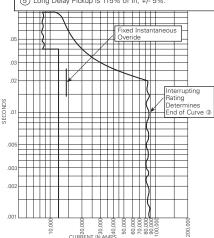
UL/CSA rms Sym. kA, 50/60 Hz				IEC 60947-2 rms Sym. kA, 50/60 Hz		
Breaker Type	240V	480V	600V	Breaker Type	240/240V	380/415V
ND, CND	65	50	25	ND, CND	65	50
HND, CHND	100	65	35	HND, CHND	100	65
NDC, CNDC	200	100	50	NDC, CNDC	200	100
Notoo				ICS = .25 ICU, ICW =	= 15 kA @ .5S, U _{imp} =	8kV

Notes:

Curve accuracy applies from -20°C to +55°C ambient. For possible ampere derating for ambient above 40°C, refer to Eaton.

Digitrip RMS 310 trip units are suitable for functional field testing with test kit Cat. No. STK2. For field testing using primary injection methods, follow NEMA publication AB-4.

- ① There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pick up value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time delay reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.
- (2) For high fault current levels a fixed instantaneous override is provided at 14000A (Tolerance ±15%).
- ③ The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.
- ④ Not UL/CSA Listed.
- 5 Long Delay Pickup is 115% of In, +/- 5%





Time Current Curves TD012029EN Effective February 2016

Types ND, CND, HND, CHND, NDC, CNDC, NDU, NGU Equipped With Type NES Digitrip RMS 310 Trip Units With Adjustable Short Time Delay (Phase Protection)

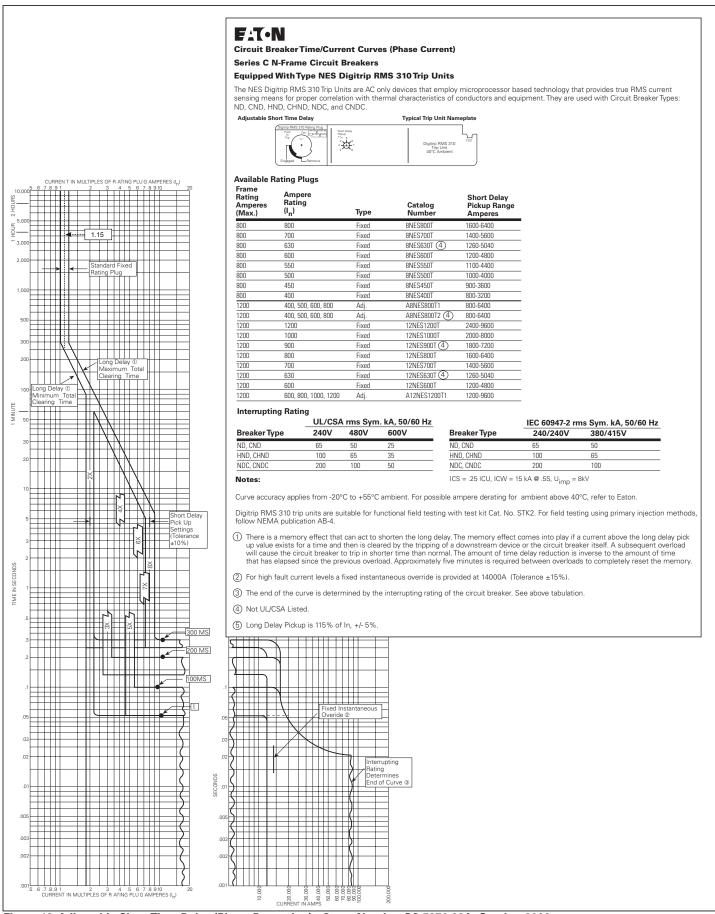


Figure 13. Adjustable Short Time Delay (Phase Protection) - Curve Number SC-5376-92A, October 2006 16 EATON www.eaton.com

Types ND, CND, HND, CHND, NDC, CNDC, NDU, Equipped With Type NES Digitrip RMS 310 Trip Units With Ground Fault Protection

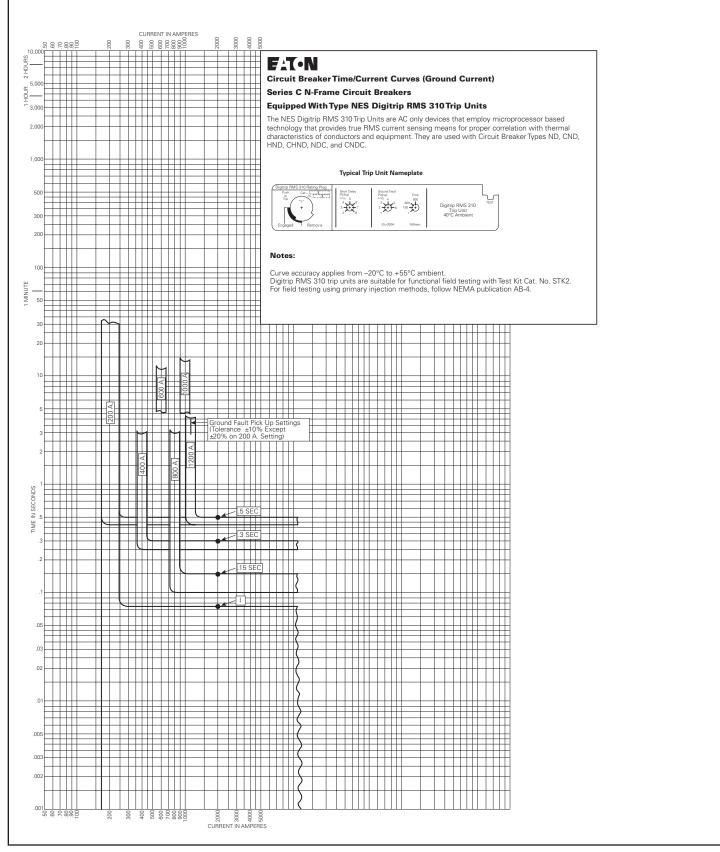


Figure 14. Ground Fault Protection - Curve Number SC-5377-92A, October 2009

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I²t, Short Delay I²t

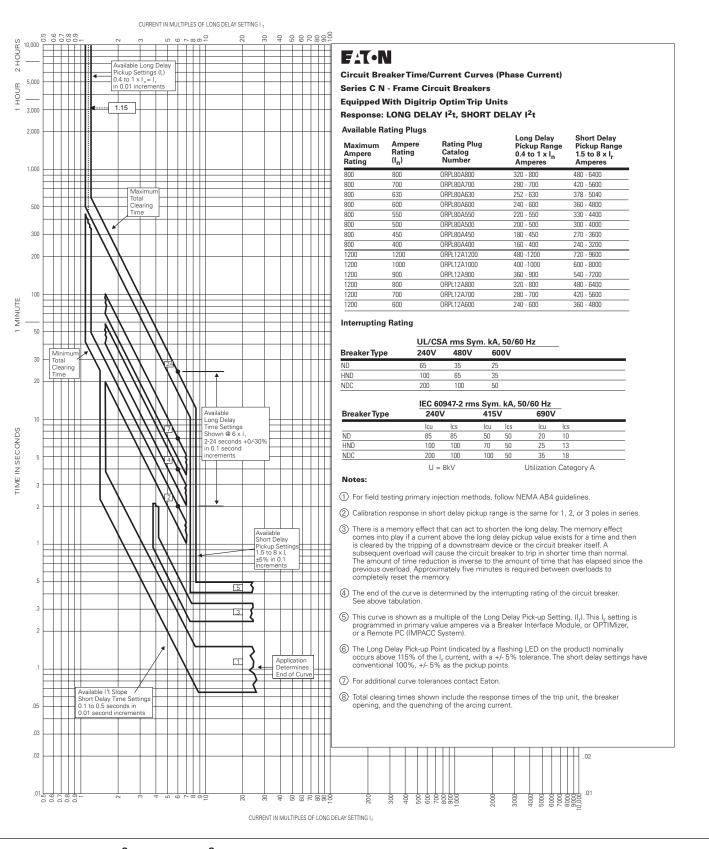
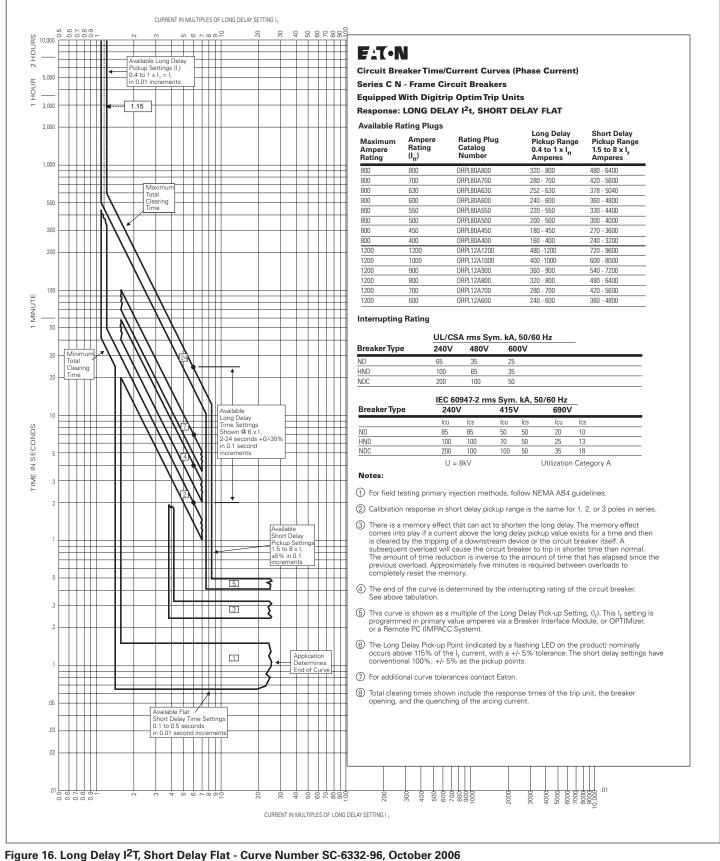


Figure 15. Long Delay I²T, Short Delay I²T - Curver Number SC-6331-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I²t, Short Delay Flat



N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Long Delay I⁴t, Short Delay Flat

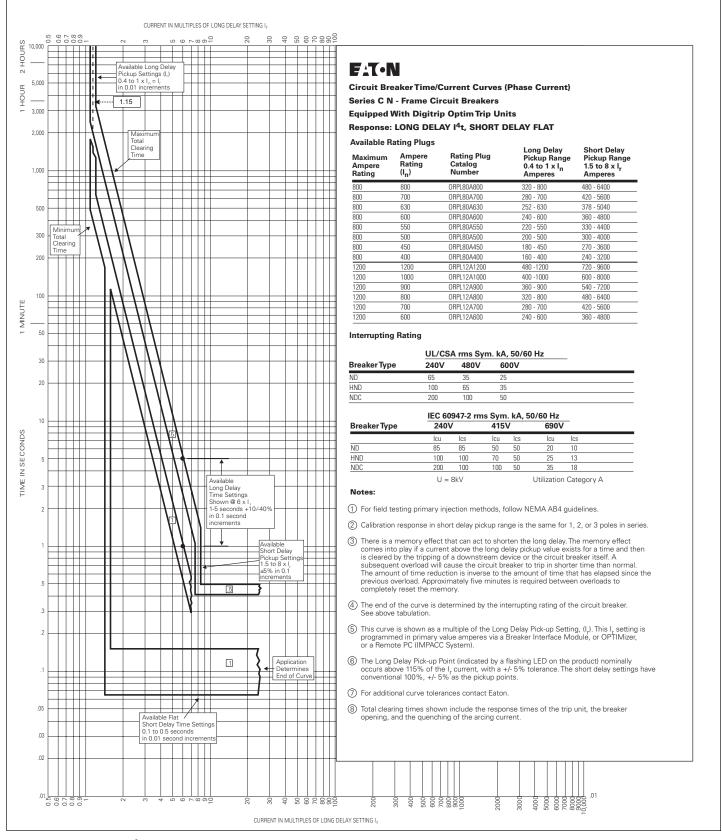
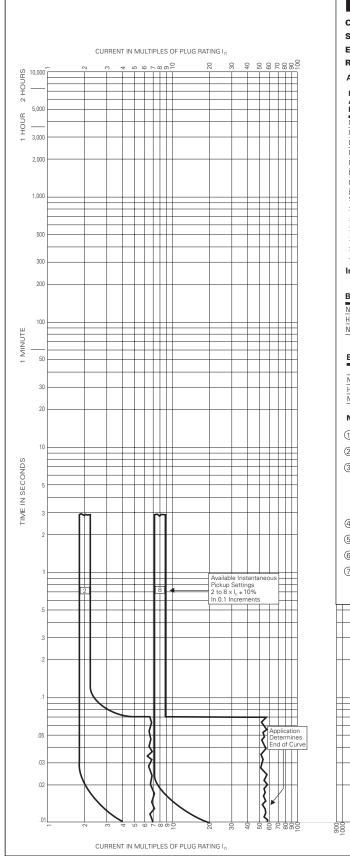


Figure 17. Long Delay I⁴T, Short Delay Flat - Curve Number SC-6333-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Instantaneous and Override



F:T•N

Circuit Breaker Time/Current Curves (Phase Current)

Series C N - Frame Circuit Breakers

Equipped With Digitrip Optim Trip Units

Response: INSTANTANEOUS AND OVERRIDE

Available Rating Plugs

Available R	ating Plugs		Law Dalay	Oh and Dallan
Maximum Ampere Rating	Ampere Rating (I _n)	Rating Plug Catalog Number	Long Delay Pickup Range 0.4 to 1 x I _n Amperes	Short Delay Pickup Range 1.5 to 8 x I _r Amperes
800	800	ORPL80A800	320 - 800	480 - 6400
800	700	ORPL80A700	280 - 700	420 - 5600
800	630	ORPL80A630	252 - 630	378 - 5040
800	600	ORPL80A600	240 - 600	360 - 4800
800	550	ORPL80A550	220 - 550	330 - 4400
800	500	ORPL80A500	200 - 500	300 - 4000
800	450	ORPL80A450	180 - 450	270 - 3600
800	400	ORPL80A400	160 - 400	240 - 3200
1200	1200	ORPL12A1200	480 -1200	720 - 9600
1200	1000	ORPL12A1000	400 -1000	600 - 8000
1200	900	ORPL12A900	360 - 900	540 - 7200
1200	800	ORPL12A800	320 - 800	480 - 6400
1200	700	ORPL12A700	280 - 700	420 - 5600
1200	600	ORPL12A600	240 - 600	360 - 4800

Interrupting Rating

	UL/CS/			
Breaker Type	240V	480V	600V	_
ND	65	35	25	
HND	100	65	35	
NDC	200	100	50	

IEC 60947-2 rms Sym. kA, 50/60 Hz

Breaker Type	240\	240V		415V		690V		
	lcu	lcs	lcu	lcs	lcu	lcs		
ND	85	85	50	50	20	10		
HND	100	100	70	50	25	13		
NDC	200	100	100	50	35	18		
Notes:	U = 8kV			Utilization	n Categor	yА		

① For field testing primary injection methods, follow NEMA AB4 guidelines.

(2) Calibration response in short delay pickup range is the same for 1, 2, or 3 poles in series.

③ There is a memory effect that can act to shorten the long delay. The memory effect comes into play if a current above the long delay pickup value exists for a time and then is cleared by the tripping of a downstream device or the circuit breaker itself. A subsequent overload will cause the circuit breaker to trip in shorter time than normal. The amount of time reduction is inverse to the amount of time that has elapsed since the previous overload. Approximately five minutes is required between overloads to completely reset the memory.

(4) The end of the curve is determined by the interrupting rating of the circuit breaker. See above tabulation.

(5) The instantaneous settings have conventional 100%, +/- 10% as the pickup points.

6 For additional curve tolerances contact Eaton.

 \bigodot Total clearing times shown include the response times of the trip unit, the breaker opening, and the quenching of the arcing current.

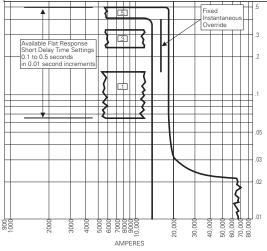


Figure 18. Instantaneous and Override - Curve Number SC-6334-96, October 2006

N-Frame Circuit Breakers Equipped with Digitrip OPTIM Trip Units; Ground Fault or Ground Fault Alarm Only

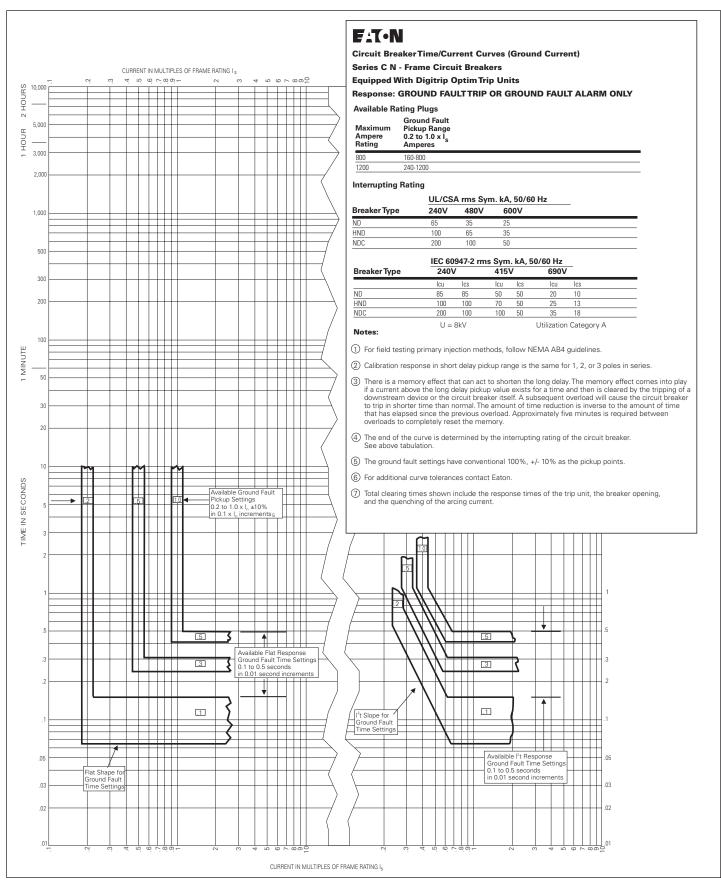


Figure 19. Ground Fault Protection - Cuver Number SC-6335-96, October 2006

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